Attorney's Docket No.:10559-391001/P10256 - ADI APD1812-1-US

This listing of claims replaces all prior versions, and listings, of claims in the application:

(Currently Amended) A method comprising: 1.

fetching, by a first pipeline of a pipelined processor defining, within a computer program, loop conditions corresponding to a particular instance of a loop setup instruction for a first hardware loop;

first propagating a first of said loop conditions of said first hardware loop corresponding to the particular instance of a loop setup instruction via said a first pipeline of a pipelined processor;

piping a second of said loop conditions from said first pipeline of the pipelined processor to a second pipeline of the pipelined processor; and

second propagating in parallel with said first propagating the a second of said loop conditions for said first hardware loop corresponding to the particular instance of a loop setup instruction via said a second pipeline of the pipelined processor.

(Previously Presented) The method of claim 1, further 2. comprising: